

ADTST.031AUS



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Hiroaki Yamoto et al.)	Group Art Unit 2825
Serial No.	:	09/941,396)	
Filed	:	August 28, 2001)	
For	:	METHOD FOR DESIGN VALIDATION OF COMPLEX IC)	
Examiner	:	Vuthe Siek)	

AMENDMENT

Hon. Commissioner
of Patents and Trademarks
Alexandria, VA 22313-1450

Dear Sir:

This amendment is submitted in response to the office action from the U.S. Patent and Trademark Office mailed October 18, 2004 for the above-identified patent application. Please make the following changes: